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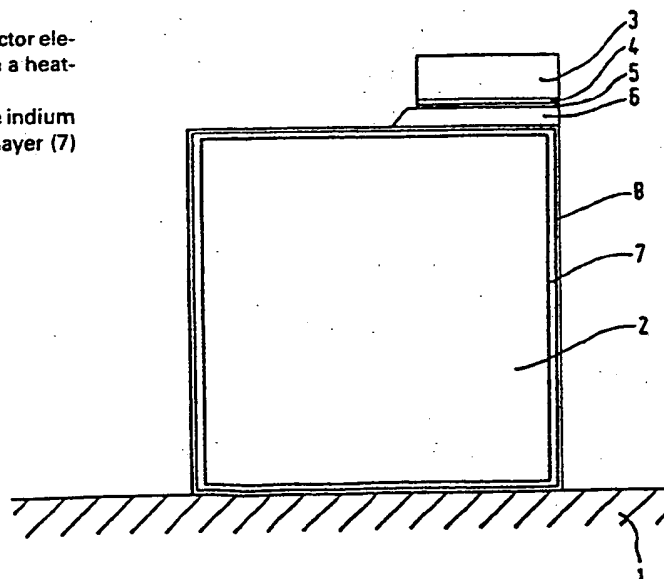
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⑤④ Semiconductor device comprising a heat sink.

⑤⑦ A semiconductor device comprising a semiconductor element (3) is secured by means of indium solder (6) on a heat-dissipating support (2) of copper or a copper alloy.

There is provided between the support (2) and the indium solder (6) a barrier layer, which consists of a cobalt layer (7) coated with an extremely thin gold layer (8).



Semiconductor device

TITLE MODIFIED  
see front page

The invention relates to a semiconductor device comprising a semiconductor element which is secured by means of indium solder on a heat-dissipating support of copper or a copper alloy, a barrier layer being formed between the support and the indium solder.

5 Indium solder has the advantage that the melting temperature is comparatively low. It is used, for example, for soldering semiconductor laser elements. The semiconductor laser elements are not allowed to reach a high temperature; the support, which has to ensure a good heat dissipation, is chosen to be made mostly of copper. However, 10 indium diffuses into copper so that a barrier layer has to be formed between the support and the solder. It is known to use a barrier layer of molybdenum. However molybdenum cannot readily be provided and the cost price of molybdenum is high. Platinum is also suggested to be used as a barrier layer. However, platinum has the same disadvantages as 15 molybdenum.

The invention has for its object to provide a semiconductor device of the kind mentioned in the opening paragraph, which has a barrier layer which acts effectively, can be provided in a simple manner and is inexpensive. It is a surprise to find that a very 20 effective barrier between indium and a support of copper or a copper alloy is obtained if the barrier layer consists of a cobalt layer coated with a very thin gold layer. The cobalt layer prevents the diffusion of indium into the copper of the support, while the thin gold layer ensures a good wetting during soldering.

25 In a favourable embodiment of the invention, the cobalt layer has a thickness of at least  $2\mu\text{m}$  and the gold layer has a thickness lying between  $0.05\mu\text{m}$  and  $0.1\mu\text{m}$ .

According to a further embodiment of the invention, the cobalt layer and the gold layer are applied by electrodeposition. The 30 fact that the cobalt can be applied by electrodeposition means a considerable simplification in the manufacture with respect to other barrier materials, such as molybdenum.

The Figure shows diagrammatically a support with a semiconductor element secured on it, while for the sake of clarity the parts of the Figure are not all drawn to scale.

A support 2 for a semiconductor element 3 is secured on a part 1 of an envelope (not shown) of the semiconductor device. The semiconductor element 3 may be, for example, a semiconductor laser element, which can be composed in known manner of a plurality of layers of gallium arsenide and gallium aluminium arsenide. For a soldering connection, the major surface to be soldered of such an element is provided, for example, with a molybdenum layer 4, to which a gold layer 5 is applied.

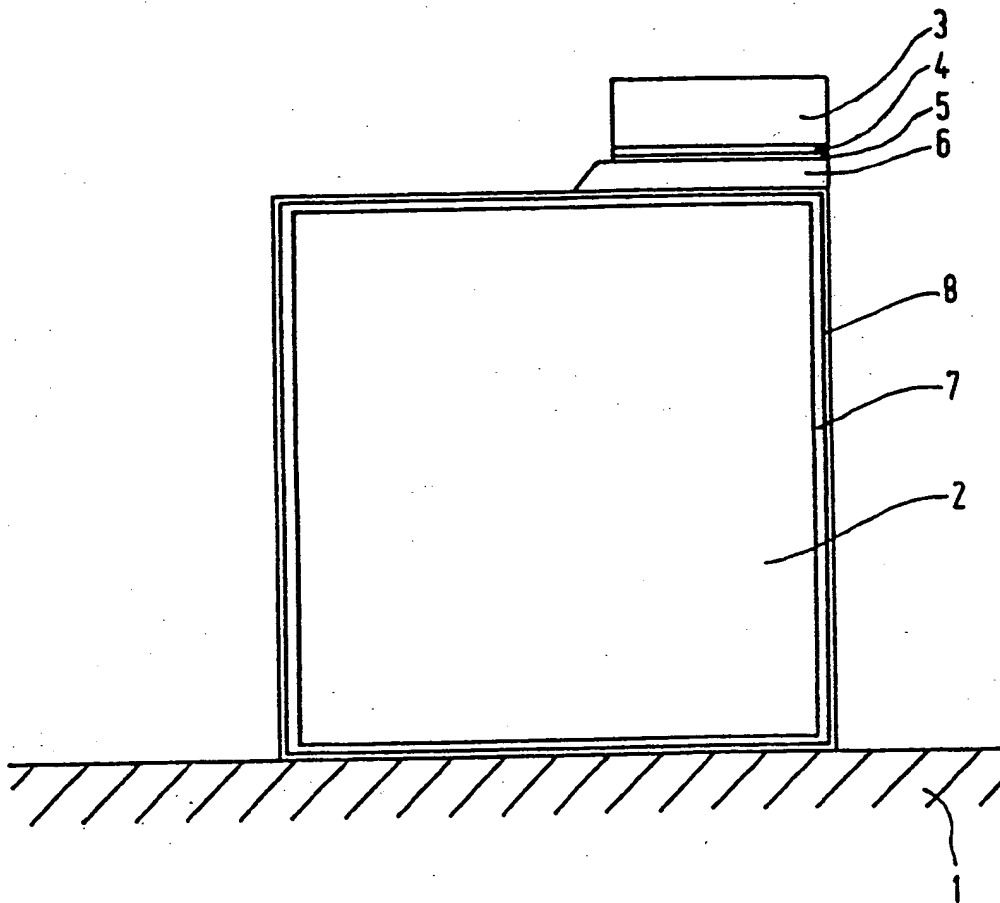
The support is made of copper or of a copper alloy. A good dissipation of the heat developed in the laser element 3 is then ensured. The semiconductor laser element is secured to the support by indium solder 6. The use of the indium has the advantage that the melting temperature during soldering may be comparatively low, i.e. of the order of  $160^{\circ}\text{C}$  to  $200^{\circ}\text{C}$ , depending upon the composition. However, indium solder tends to diffuse into copper. In order to avoid the soldering layer disappearing entirely in the copper, a barrier layer has to be provided.

It is a surprise to find that a layer of cobalt forms a particularly suitable barrier against diffusion of indium into copper. A cobalt layer 7 is applied to the support 2 and a very thin gold layer 8 is applied to the cobalt layer 7. The gold layer 8 ensures a good wetting during soldering. The layers 7 and 8 can be advantageously applied by electrodeposition; they can be present over the whole surface of the support. It is already sufficient when the layers 7 and 8 are present only at the area of the indium solder. However the process of applying the layers thus becomes more complicated. It will be appreciated that the layers 7 and 8 may also be deposited in a different manner, for example by vapour deposition. However, this process is generally more laborious than electrodeposition. It is recommendable, at any rate if the barrier layer is applied only locally to the support, to first apply to the support a thin layer of nickel or nickel phosphorus in order to prevent oxidation of the copper.

Experiments have shown that a barrier layer having a thickness of the cobalt layer of  $2\mu\text{m}$  or more and of the gold layer of  $0.05$  to  $0.1\mu\text{m}$  yields particularly favourable results.

**CLAIMS**

1. A semiconductor device comprising a semiconductor element which is secured by means of indium solder on a heat-dissipating support of copper or a copper alloy, a barrier layer being provided between the support and the indium solder, characterized in that the barrier layer consists of a cobalt layer coated with a very thin gold layer.
2. A semiconductor device as claimed in Claim 1, characterized in that the cobalt layer has a thickness of at least 2  $\mu\text{m}$  and in that the gold layer has a thickness lying between 0.05  $\mu\text{m}$  and 0.1  $\mu\text{m}$ .
- 10 3. A semiconductor device as claimed in Claim 1 or 2, characterized in that the cobalt layer and the gold layer are applied by electrodeposition.





European Patent  
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# EUROPEAN SEARCH REPORT

0201954

Application number

EP 86 20 0648

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
X	DE-A-2 027 945 (PHILIPS) * Claims 1-3; page 1, paragraph 2; page 4, paragraph 2 *	1-3	H 01 L 23/36 H 01 L 23/48
A	FR-A-2 118 101 (PHILIPS)		
A	US-A-4 424 527 (OPTICAL INFORM. SYST.)		
A	US-A-4 349 585 (NGK)		
A	SOLID-STATE AND ELECTRONIC DEVICES, vol. 3, no. 6, November 1979, pages 206-209; R.G. PLUMB et al.: "Thermal-impedance ageing characteristics of c.w. stripe lasers"		
			TECHNICAL FIELDS SEARCHED (Int. Cl. 4)
			H 01 L
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 03-07-1986	Examiner DE RAEVE R.A.L.
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons A : member of the same patent family, corresponding document</p>			